

Abstract of Disclosure

A wobble clock generator with a protective mechanism that can avoid interference generated from a phase-modulated wobble signal. The wobble clock generator has an arithmetic/logic circuit and a phase-locked loop. The arithmetic/logic circuit calculates a period count value by counting a period of a wobble signal according to a reference clock, and compares an average value with the period count value for outputting a control signal. The phase-locked loop is electrically connected to the arithmetic/logic circuit for generating a wobble clock according to the control signal and the wobble signal. When the control signal corresponds to a first logic level, the phase-locked loop compares the wobble signal with the wobble clock to drive the wobble clock to be synchronized with the wobble signal. When the control signal corresponds to a second logic level, the phase-locked loop holds the wobble signal without synchronizing the wobble clock with the wobble signal.

20 Figures